

Information note

INF213305

Dear

With this Infineon Technologies AG Information Note we would like to inform you about the following

Introduction of die overcoat for FL-S and GL-S KGD NOR Flash Memory products at Cypress Semiconductor Corporation (Fab 25), Austin, USA



On 16 April 2020, Infineon acquired Cypress.
We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.
For further details, please visit our website:

<https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/>

Cypress Semiconductor Corporation – An Infineon Technologies AG company,
198 Champion Court San Jose, CA 95134. Tel: (408) 943-2600

► **Products affected**

Please refer to attached affected product list [18]

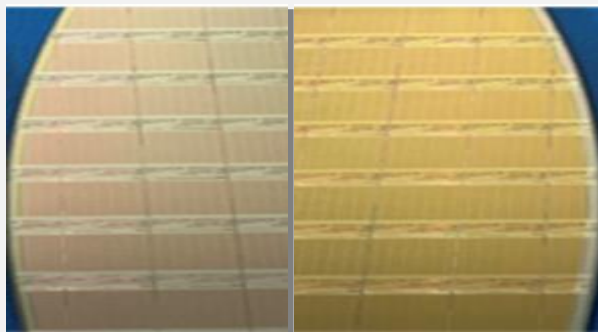
► **Detailed change information**

Subject Introduction of die overcoat for FL-S and GL-S KGD NOR Flash Memory products at Cypress Semiconductor Corporation (Fab 25), Austin, USA

Reason The die overcoat (PBO) will provide a higher level of protection to the silicon surface during production (fab, sort and assembly). As a result, it will minimize the risk of latent failures due to surface damage. Functionality and specifications remain unchanged.

Description	<u>Old</u>	<u>New</u>
	<ul style="list-style-type: none"> 65nm Non-Automotive Known Good Die (KGD) products from Fab 25 shipped without die overcoat (Non PBO) 	<ul style="list-style-type: none"> 65nm Non-Automotive Known Good Die (KGD) products from Fab 25 shipped with or without die overcoat (PBO)

► **Product identification**



Infineon maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

► **Impact of change**

Products manufactured are completely compatible with existing product from form, fit, functional, parametric, and quality performance perspectives.

Infineon also recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

► **Attachments**

Affected product list [18]
Supporting Documents:

► **Intended start of delivery**

Standard MPNs: 90 days from PIN date.

If you have any questions, please do not hesitate to contact your local sales office.

[Information Note] N° [INF213305]

Introduction of die overcoat for FL-S and GL-S KGD
NOR Flash Memory products at Cypress Semiconductor
Corporation (Fab 25), Austin, USA



Item	Marketing Part Number	Family
1	S25FL512S0SWEI009	NOR
2	S29GL01GS11WEI019	NOR
3	S29GL01GS11WEI029	NOR
4	S29GL128S10WEI019	NOR
5	S29GL128S10WEI029	NOR
6	S29GL256S10WEI019	NOR
7	S29GL256S10WEI029	NOR
8	S29GL512S11WEI019	NOR
9	S29GL512S11WEI029	NOR
10	S29GL512S11WEIV19	NOR
11	S99-50298	NOR
12	S99-50299	NOR
13	S99-50300	NOR
14	S99-50335	NOR
15	S99GL01GS11WEI029	NOR
16	S99GL064S90WEI019	NOR
17	S99GL064S90WEI039	NOR
18	S99GL064S90WEI049	NOR