

PDTB123YT-Q

PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

25 February 2022

Product data sheet

1. General description

500 mA PNP Resistor-Equipped Transistor (RET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

NPN complement: PDTD123YT-Q.

2. Features and benefits

- 500 mA output current capability
- Reduces pick and place costs
- · Built-in bias resistors
- ±10 % resistor ratio tolerance
- Simplifies circuit design
- Reduces component count
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- · Digital application in automotive and industrial segments
- · Cost-saving alternative for BC807 series in digital applications
- · Control of IC inputs
- Switching loads

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
Io	output current		-	-	-500	mA
R1	bias resistor 1 (input)	T _{amb} = 25 °C	1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4.1	4.55	5	



PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	I	input (base)	3	
2	GND	ground (emitter)		R1
3	0	output (collector)	SOT23	GND R2 R2

6. Ordering information

Table 3. Ordering information

Type number Package						
	Name	Description	Version			
PDTB123YT-Q	SOT23	plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23			

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PDTB123YT-Q	%7Y

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
VI	input voltage	positive		-	5	V
		negative		-	-20	V
Io	output current			-	-500	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{\text{th(j-a)}}$	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	I	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -40 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-	-100	nA
	current	V _{CB} = -50 V; I _E = 0 A; T _{amb} = 25 °C	-	-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -50 V; I _B = 0 A; T _{amb} = 25 °C	-	-	-	-0.5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A; T _{amb} = 25 °C	-	-	-	-0.65	mA
h _{FE}	DC current gain	V_{CE} = -5 V; I_{C} = -50 mA; T_{amb} = 25 °C	-	70	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	-	-300	mV
$V_{I(off)}$	off-state input voltage	V _{CE} = -5 V; I _C = -100 μA; T _{amb} = 25 °C	-	-0.4	-0.6	-1	V
V _{I(on)}	on-state input voltage	V_{CE} = -0.3 V; I_{C} = -20 mA; T_{amb} = 25 °C	-	-0.5	-1	-1.4	V
R1	bias resistor 1 (input)	T _{amb} = 25 °C		1.54	2.2	2.86	kΩ
R2/R1	bias resistor ratio		4	4.1	4.55	5	
C _c	collector capacitance	V_{CB} = -10 V; I_{E} = 0 A; i_{e} = 0 A; f = 100 MHz; T_{amb} = 25 °C	-	-	11	-	pF

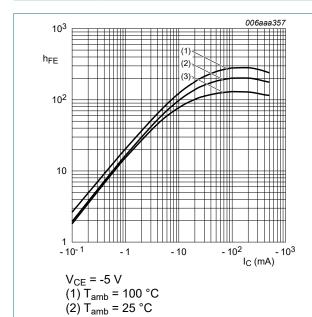


Fig. 1. DC current gain as a function of collector current; typical values

(3) $T_{amb} = -40 \, ^{\circ}C$

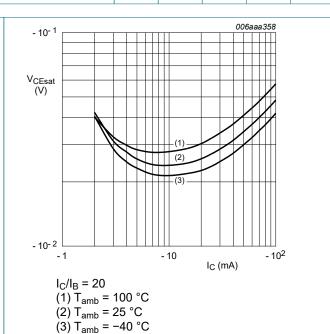
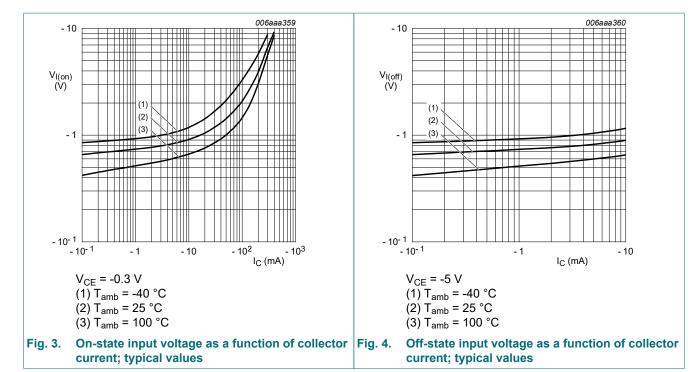


Fig. 2. Collector-emitter saturation voltage as a function of collector current; typical values

PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω



PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

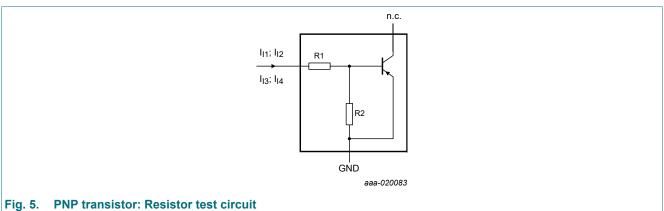
Resistor calculation

• Calculation of bias resistor 1 (R1)

$$R1 = \frac{V(I12) - V(I11)}{I12 - I11}$$

· Calculation of bias resistor ratio (R2/R1)

$$\frac{R2}{R1} = \frac{V(I14) - V(I13)}{R1 \cdot (I14 - I13)} - 1$$



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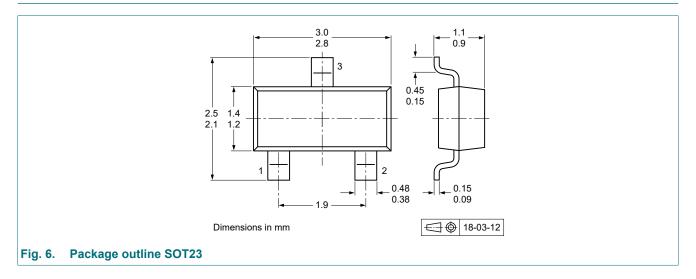
Resistor test conditions

Table 8. Resistor test conditions

Type number	R1 (kΩ)	R2 (kΩ)	Test conditions				
			I _{I1}	I ₁₂	I ₁₃	I ₁₄	
PDTB123YT-Q	2.2	10	-0.7 mA	-0.8 mA	0.45 mA	0.55 mA	

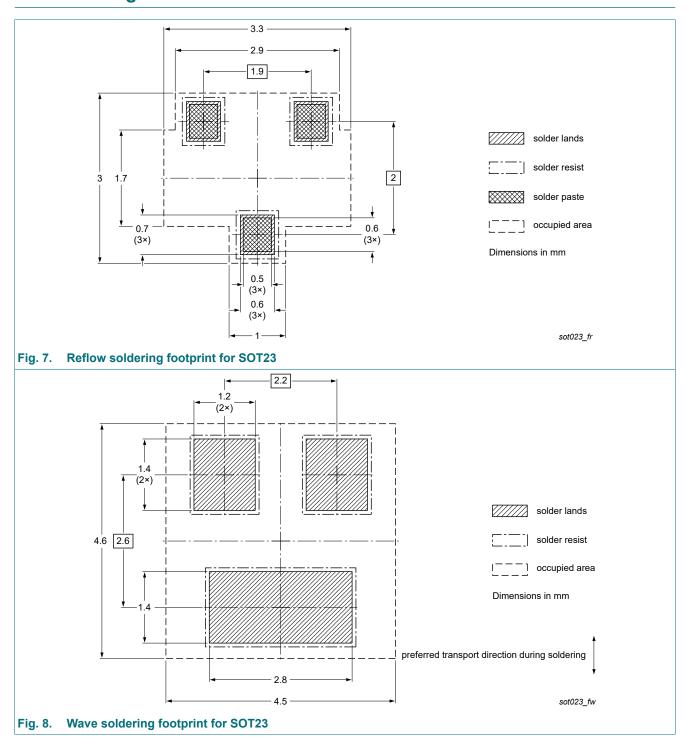
PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

12. Package outline



PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

13. Soldering



PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PDTB123YT-Q v.1	20220225	Product data sheet	-	-

PNP 500 mA, 50 V resistor-equipped transistor; R1 = 2.2 k Ω , R2 = 10 k Ω

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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