



800 W. 6th Street, Austin, TX 78701

PCN-2014-562
CS8420-CSZ(R)/D1, CS8420-DSZ(R)/D1, CS8416-CSZ(R)/E, CS8416-DSZ(R)/E
Assembly Tooling Change
Information Only

Date: 14th October 2014

Dear Customer:

This is an Information Only announcement for an assembly tooling change on CS8420-CSZ(R)/D1, CS8420-DSZ(R)/D1, CS8416-CSZ(R)/E, CS8416-DSZ(R)/E material.

The changes discussed within this PCN are for your information only. Please see the attachment details and supporting documents.

This notification is per the Cirrus Logic standard communication process. Any inquiries should be directed to your local Field Sales Representative.

Sincerely,

Cirrus Logic Corporate Quality
Phone: +1(512) 851-4000

Attachment: 1

Products Affected:

CS8420-CSZ(R)/D1, CS8420-DSZ(R)/D1, CS8416-CSZ(R)/E, CS8416-DSZ(R)/E

The devices listed on this page are the complete list of affected devices. According to our records, these are the devices that you have purchased within the past twenty-four (24) months. The corresponding customer part number is also listed, if available.

Technical details of this assembly tooling change follow on the next page(s).

PCN Number:		2014-562		PCN Date:		October 2014	
Title:		Information Only announcement of Assembly Tooling Change on CS8420-CSZ(R)/D1, CS8420-DSZ(R)/D1, CS8416-CSZ(R)/E, CS8416-DSZ(R)/E material currently offered by Cirrus Logic.					
Customer Contact:		Local Field Sales Representative		Phone:		(512) 851-4000	
				Dept:		Corporate Quality	
Proposed 1st Ship Date:		Not Applicable		Estimated Sample Availability date:		Not Applicable	
Change Type:		Assembly Tooling Change. Change Type = Minor					
<input type="checkbox"/>	Assembly Site	<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Assembly Materials		
<input type="checkbox"/>	Design	<input type="checkbox"/>	Electrical Specification / Data Sheet	<input type="checkbox"/>	Mechanical Specification		
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process		
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material	<input type="checkbox"/>	Wafer Bump Process		
<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials	<input type="checkbox"/>	Wafer Fab Process		

PCN Details	
Description of Change:	
<p>The following items related to the assembly tooling change are:</p> <ul style="list-style-type: none"> • Single strand format from (12 units per strip) → to a Matrix format (32 units per strip) • Flat backside flag design → Dimpled backside flag design 	
Reason for Change:	
<p>Cirrus Logic was informed by our assembly subcontractor (ANST) they will obsolete the single strand format and replace with a Matrix format. Along with the format change, additional performance improvements were made.</p> <ul style="list-style-type: none"> • Matrix format increases unit count per strip from 12 to 32, resulting in manufacturing efficiency improvement. • Dimple design features on the flag backside serve as mold-lock features to improve backside flag to mold compound adhesion. 	

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

Anticipated No Adverse Impact to the Quality and/or Reliability of said product.

Product Affected: **Table I**


Customer Material Part Number Cirrus Logic Part Number


CS8420-CSZ/D1
CS8420-CSZR/D1
CS8420-DSZ/D1
CS8420-DSZR/D1
CS8416-CSZ/E
CS8416-CSZR/E
CS8416-DSZ/E
CS8416-DSZR/E

Changes to product identification resulting from this PCN:

The Cirrus Logic component symbolization on the external face of the device reflects the designated Assembly Site, which remains as is with no change.

TOP SIDE BRAND

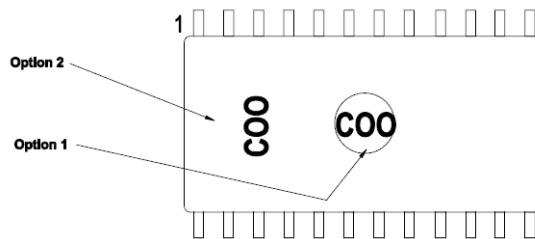
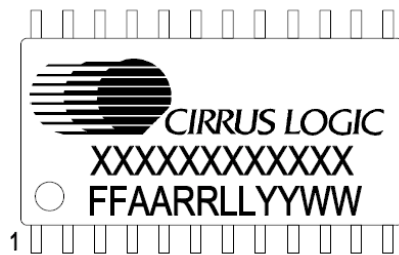
Logo Line: Use  CIRRUS LOGIC
logo as shown. See Note 1 below.
Line 1: Part Number (12 spaces max.)
Line 2: Package Mark (12 spaces max.)

Note 1: The font used in the  CIRRUS LOGIC
logo is non-standard and part of our trademark.
This must be reproduced as a graphic image on
our top mark.

BACKSIDE BRAND:

COO = COUNTRY OF ORIGIN (Not required if molded
into back of package). [See Note 2 below.]

Note 2: The Country Of Origin (COO) may be molded
into the package on the back side (Option 1) or it may be
marked as shown (Option 2). It may also be molded into
the front side (not shown).



The Qualification Plans are designed using AEC, JEDEC, and other applicable industry standards.

CS8416-CSZ/E Qualification

CS8416-CSZ/E Qualification: Plan Test Results

Parameter	Comparison Basis	Sample Size	Success Criteria	Test Results
Wire Pull	JEDEC criteria	1 Lot (22 wires)	>3g	Pass
Ball Shear	JEDEC criteria	1 Lot (22 wires)	>15g	Pass
Die Shear	JEDEC criteria	1 Lot (5 units)	>5kg	Pass
Physical Dimen	Case Outline	1 Lot (22 units)	Per package drawing	Pass
Solderability	JEDEC criteria	2 Lots (5 units each)	Wetting area >95%	Pass
MSL 3 + 260C Precon	JEDEC criteria	1 Lot (270 units)	Pass electrical test	0/270
MSL 3 + 260C Precon	JEDEC criteria	3 Lot (22 units each)	No delamination	Pass
MSL 3 + 260C Precon + 500 TC	JEDEC criteria -65C <-> +150C	1 Lot (100 units)	Pass electrical test	0/100
Post TC Wire Pull	AEC Q100	5 units	>3g	0/5

Notes:

- Qualification tests “pass” on zero fails for each test
- CS8416-CSZ/E serves as the Qualification Vehicle for CS8420-CSZ(R)/D1, CS8420-DSZ(R)/D1, and CS8416-DSZ(R)/E.

Reliability Qualification Results:

The Reliability Qualification testing has successfully completed and passed all testing.