

Features

- · Excellent Stability and Uniformity
- · Split Gate Trench Mosfet Technology
- Lower R_{DS(ON)}
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free ."Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Moisture Sensitivity Level 1

Maximum Ratings

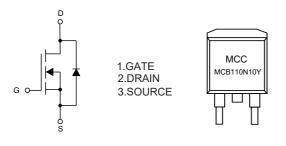
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 0.48°C/W Junction to Case

Parameter		Symbol	Rating	Unit	
Drain -Source Voltage		V _{DS}	100	V	
Gate -Source Volltage		V _{GS}	±20	V	
Drain Current-Continuous	T _C =25°C	- I _D	110	A	
	T _C =100°C		70		
Drain Current-Pulse(Note2)		I _{DM}	440	Α	
Power Dissipation		P _D	260	W	
Single Pulsed Avalanche Energy ^(Note3)		E _{AS}	400	mJ	

Notes:

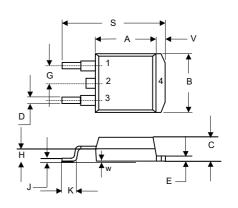
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. Pulse Width Limited by Maximum Junction Temperature.
- 3. EAS Condition: $T_J=25^{\circ}C$, $V_{DD}=50V$,Rg= 25Ω ,L=2mH,I_{AS}=31A.

Internal Structure and Marking Code



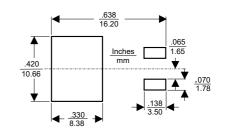
N-CHANNEL MOSFET

D2-PAK



DIMENSIONS					
DIM INCHES MIN MAX		MM		NOTE	
		MAX	MIN MAX		NOTE
Α	0.331	0.370	8.40	9.40	
В	0.378	0.417	9.60	10.60	
O	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
Е	0.045	0.055	1.14	1.40	
G	0.010		2.54		TYP.
Η	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

Suggested Solder Pad Layout





Electrical Characteristics @ 25°C (Unless Otherwise Noted)

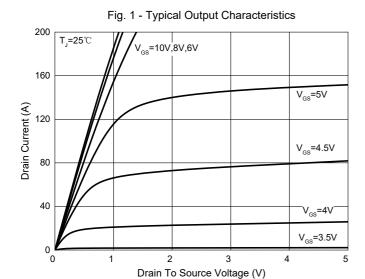
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics	<u>. </u>				<u> </u>		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} =0V, I_{D} =250 μ A	100			V	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V,T _J =25°C			1	uA	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0	2.8	4.0	V	
Drain-Source On-Resistance (Note4)	R _{DS(on)}	V _{GS} =10V, I _D =20A		4.5	5	mΩ	
Gate resistance	R_{G}	V _{GS} =0V,f=1MHz		0.9		Ω	
Body Diode Voltage	V _{SD}	I _{SD} =20A, V _{GS} =0V			1.3	V	
Dynamic Characteristics(Note 5))						
Input Capacitance	C _{iss}			4600		pF	
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,f=1MHz		1250			
Reverse Transfer Capacitance	C _{rss}			43			
Total Gate Charge	Qg			66			
Gate-Source Charge	Q_{gs}	V_{DS} =50V, V_{GS} =10V, I_{D} =20A		23		" C	
Gate-Drain Charge	Q_{gd}			6.6		nC	
Reverse Recovery Charge	Q _{rr}	L _00A di/dt_400A/		93			
Reverse Recovery Time	t _{rr}	I _F =20A,di/dt=100A/μs		63			
Turn-On Delay Time	t _{d(on)}			17.6			
Turn-On Rise Time	t _r	V _{DS} =50V,I _D =20A,		30.2		ns	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =2.2 Ω		33.6			
Turn-Off Fall Time	t _f			39.6			

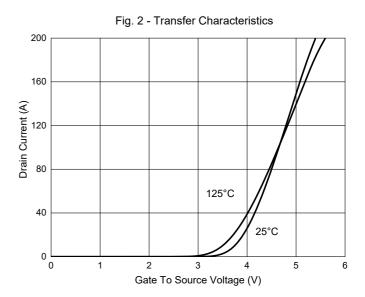
Note 4. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.

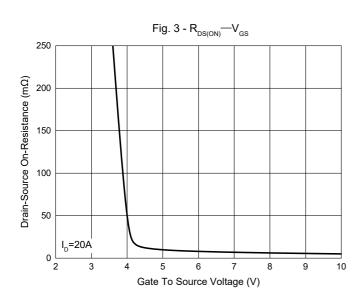
^{5.} Guaranteed by Design, Not Subject to Production Testing.

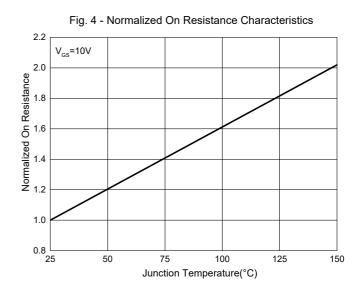


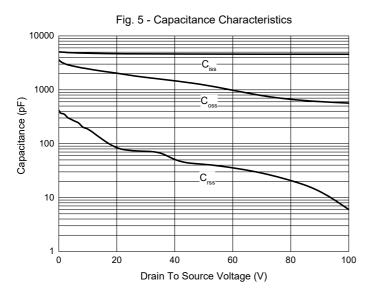
Curve Characteristics

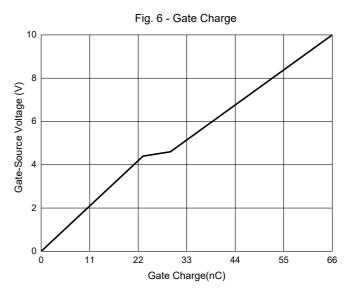














Curve Characteristics

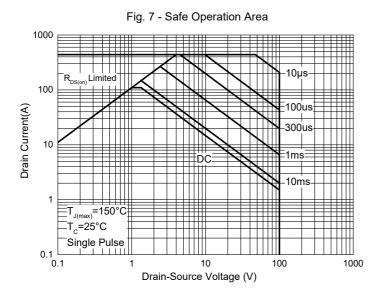
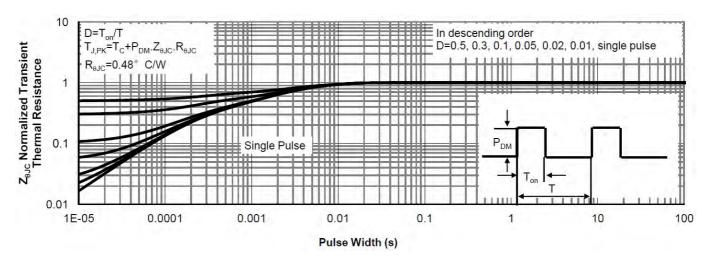


Fig.8 - Normalized Maximum Transient thermal impedance





Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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