PBSS4240X

40 V, 2 A NPN low VCEsat (BISS) transistor

15 October 2012

Product data sheet

1. Product profile

1.1 General description

NPN low V_{CEsat} Breakthrough In Small Signal (BISS) transistor in a medium power and flat lead SOT89 Surface-Mounted Device (SMD) plastic package. PNP complement: PBSS5240X.

1.2 Features and benefits

- Low collector-emitter saturation voltage V_{CEsat}
- High collector current capability I_C and I_{CM}
- · High efficiency due to less heat generation

1.3 Applications

- DC-to-DC conversion
- Supply line switching
- Battery charger
- LCD backlighting
- Driver in low supply voltage applications (e.g. lamps and LEDs)
- Inductive load driver (e.g. relays, buzzers and motors)

1.4 Quick reference data

Table 1. Quick reference data

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|---|---|-----|-----|-----|------|
| V _{CEO} | collector-emitter voltage | open base | - | - | 40 | V |
| I _C | collector current | | - | - | 2 | Α |
| I _{CM} | peak collector current | | - | - | 3 | Α |
| R _{CEsat} | collector-emitter saturation resistance | I_C = 1 A; I_B = 100 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C | - | - | 260 | mΩ |
| I _{CRM} | repetitive peak collector current | $t_p \le 20 \text{ ms}; \delta \le 0.33 \; ; \text{pulsed}$ | - | - | 2.5 | Α |



40 V, 2 A NPN low VCEsat (BISS) transistor

2. Pinning information

Table 2. Pinning information

| Pin | Symbol | Description | Simplified outline | Graphic symbol |
|-----|--------|-------------|--------------------|----------------|
| 1 | E | emitter | | C |
| 2 | С | collector | | В |
| 3 | В | base | 3 2 1 | - N |
| | | | SOT89 | sym123 |

3. Ordering information

Table 3. Ordering information

| Type number | Package | | | | |
|-------------|---------|--|---------|--|--|
| | Name | Description | Version | | |
| PBSS4240X | SOT89 | plastic surface-mounted package; die pad for good heat transfer; 3 leads | SOT89 | | |

4. Marking

Table 4. Marking codes

| Type number | Marking code |
|-------------|--------------|
| PBSS4240X | S47 |

5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|-----------------------------------|---|-----|-----|------|------|
| V _{CBO} | collector-base voltage | open emitter | | - | 40 | V |
| V _{CEO} | collector-emitter voltage | open base | | - | 40 | V |
| V _{EBO} | emitter-base voltage | open collector | | - | 5 | V |
| I _C | collector current | | | - | 2 | Α |
| I _{CRM} | repetitive peak collector current | $\delta \le 0.33$; $t_p \le 20$ ms; pulsed | | - | 2.5 | Α |
| I _{CM} | peak collector current | | | - | 3 | Α |
| I _B | base current | | | - | 300 | mA |
| I _{BM} | peak base current | | | - | 1 | Α |
| P _{tot} | total power dissipation | | [1] | - | 0.5 | W |
| | | | [2] | - | 0.95 | W |

PBSS4240X

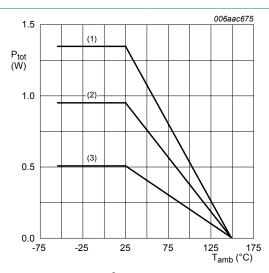
All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

40 V, 2 A NPN low VCEsat (BISS) transistor

| Symbol | Parameter | Conditions | | Min | Max | Unit |
|------------------|----------------------|------------|-----|-----|------|------|
| | | | [3] | - | 1.35 | W |
| T _j | junction temperature | | | - | 150 | °C |
| T _{amb} | ambient temperature | | | -65 | 150 | °C |
| T _{stg} | storage temperature | | | -65 | 150 | °C |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves

6. Thermal characteristics

Table 6. Thermal characteristics

| Symbol | Parameter | Conditions | | Min | Тур | Max | Unit |
|--------------------------|--|---------------|-----|-----|-----|-----|------|
| R _{th(j-a)} | thermal resistance | unction to nt | [1] | - | - | 250 | K/W |
| from junction to ambient | | | [2] | - | - | 132 | K/W |
| | ambient | | [3] | - | - | 93 | K/W |
| R _{th(j-sp)} | thermal resistance from junction to solder point | | | - | - | 16 | K/W |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

PBSS4240X

All information provided in this document is subject to legal disclaimers.

40 V, 2 A NPN low VCEsat (BISS) transistor

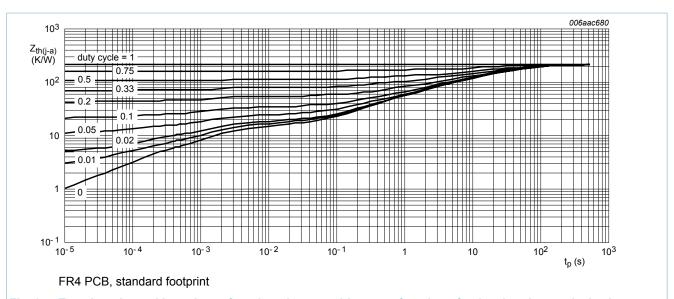


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

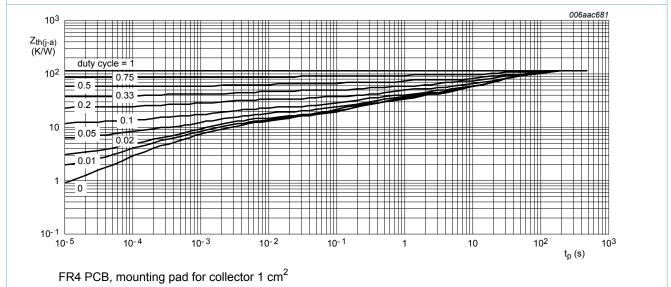
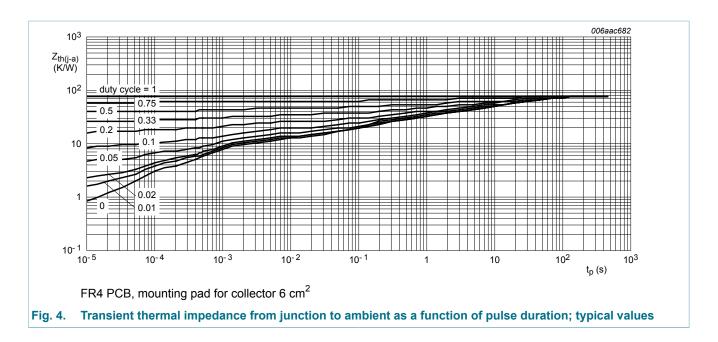


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

40 V, 2 A NPN low VCEsat (BISS) transistor



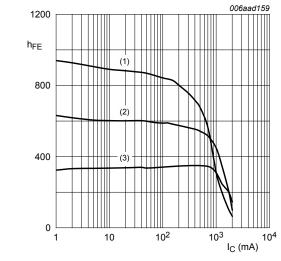
7. Characteristics

Table 7. Characteristics

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|---|--|-----|-----|-----|------|
| I _{CBO} | collector-base cut-off | V _{CB} = 40 V; I _E = 0 A; T _{amb} = 25 °C | - | - | 100 | nA |
| | current | V _{CB} = 40 V; I _E = 0 A; T _j = 150 °C | - | - | 50 | μA |
| I _{CEO} | collector-emitter cut-off current | V _{CE} = 30 V; I _B = 0 A; T _{amb} = 25 °C | - | - | 100 | nA |
| I _{EBO} | emitter-base cut-off current | V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C | - | - | 100 | nA |
| h _{FE} | DC current gain | V _{CE} = 5 V; I _C = 1 mA; T _{amb} = 25 °C | 300 | - | - | |
| | | V _{CE} = 5 V; I _C = 500 mA; T _{amb} = 25 °C | 300 | - | 900 | |
| | | V _{CE} = 5 V; I _C = 1 A; T _{amb} = 25 °C | 200 | - | - | |
| | | V_{CE} = 5 V; I_{C} = 2 A; pulsed; t_{p} ≤ 300 μs; δ ≤ 0.02 ; T_{amb} = 25 °C | 75 | - | - | |
| V _{CEsat} | collector-emitter | I _C = 100 mA; I _B = 1 mA; T _{amb} = 25 °C | - | - | 80 | mV |
| | saturation voltage | I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C | - | - | 140 | mV |
| | | I_{C} = 1 A; I_{B} = 100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{amb}$ = 25 °C | - | - | 260 | mV |
| | | I_{C} = 2 A; I_{B} = 200 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; \ T_{amb}$ = 25 °C | - | - | 510 | mV |
| R _{CEsat} | collector-emitter saturation resistance | I_{C} = 1 A; I_{B} = 100 mA; pulsed; $t_{p} \le 300 \ \mu s; \ \delta \le 0.02 \ ; T_{amb}$ = 25 °C | - | - | 260 | mΩ |

40 V, 2 A NPN low VCEsat (BISS) transistor

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|--------------------|---------------------------------|--|-----|-----|-----|------|
| V _{BEsat} | base-emitter saturation voltage | I_C = 1 A; I_B = 100 mA; pulsed; $t_p \le 300$ μs; $\delta \le 0.02$; T_{amb} = 25 °C | - | - | 1.2 | V |
| V _{BEon} | base-emitter turn-on voltage | V_{CE} = 5 V; I_{C} = 1 A; pulsed; t_{p} ≤ 300 μ s; δ ≤ 0.02 ; T_{amb} = 25 °C | - | - | 1.1 | V |
| f _T | transition frequency | V_{CE} = 10 V; I_{C} = 50 mA; f = 100 MHz; T_{amb} = 25 °C | 150 | - | - | MHz |
| C _c | collector capacitance | $V_{CB} = 10 \text{ V}; I_{E} = 0 \text{ A}; i_{e} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$ | - | - | 10 | pF |



 $V_{CE} = 5 V$

(1) T_{amb} = 150 °C

(2) T_{amb} = 25 °C

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig. 5. DC current gain as a function of collector current; typical values

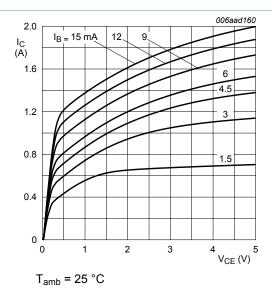
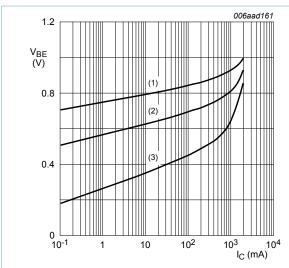


Fig. 6. Collector current as a function of collectoremitter voltage; typical values

40 V, 2 A NPN low VCEsat (BISS) transistor



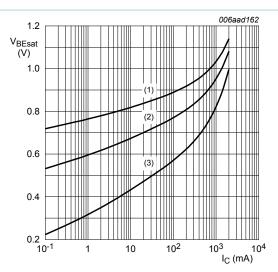
$$V_{CE} = 5 V$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb} = 25 \, ^{\circ}C$$

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



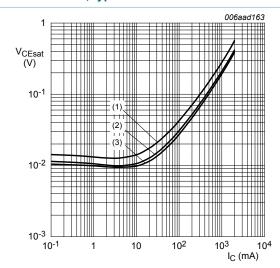
$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = -55 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = 150 \, ^{\circ}C$$

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values

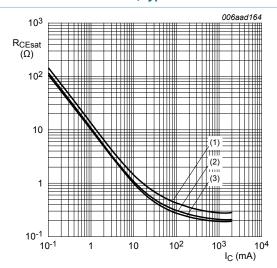


$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

(3)
$$T_{amb}$$
= -55 °C

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

(1)
$$T_{amb} = 150 \, ^{\circ}C$$

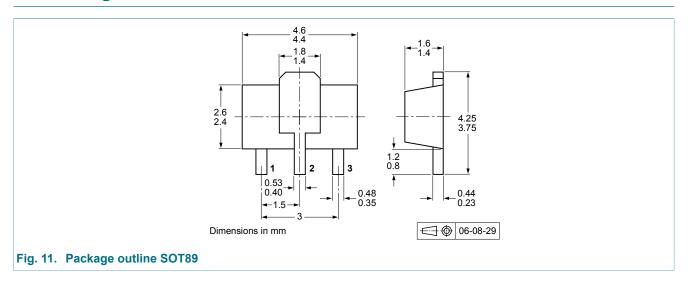
(2)
$$T_{amb}$$
 = 25 °C

$$(3) T_{amb} = -55 °C$$

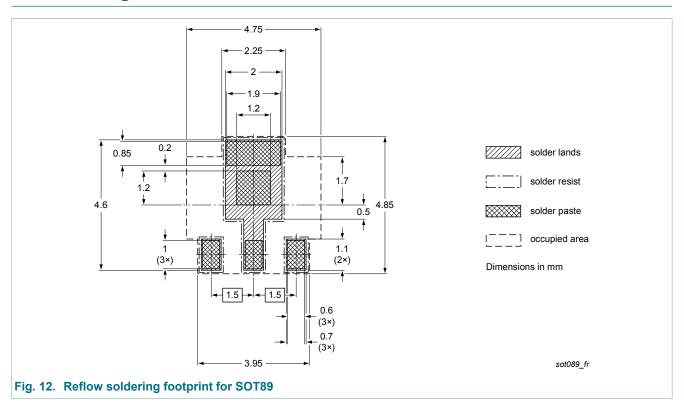
Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

40 V, 2 A NPN low VCEsat (BISS) transistor

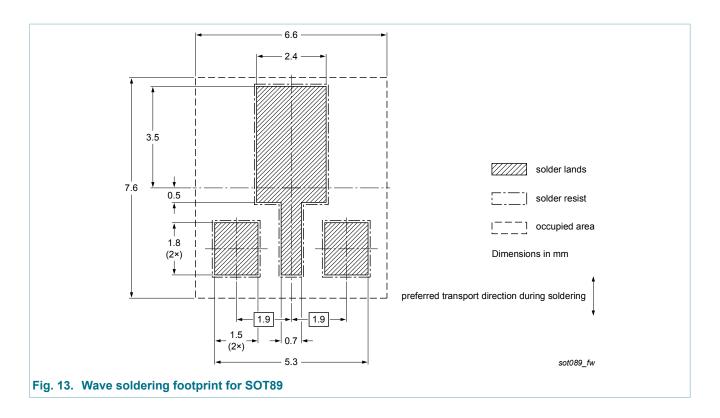
8. Package outline



9. Soldering



40 V, 2 A NPN low VCEsat (BISS) transistor



10. Revision history

Table 8. Revision history

| Data sheet ID | Release date | Data sheet status | Change notice | Supersedes |
|---------------|--------------|--------------------|---------------|------------|
| PBSS4240X v.1 | 20121015 | Product data sheet | - | - |

40 V, 2 A NPN low VCEsat (BISS) transistor

11. Legal information

11.1 Data sheet status

| Document status [1][2] | Product status [3] | Definition |
|--------------------------------------|--------------------|---|
| Objective [short] data sheet | Development | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification | This document contains data from the preliminary specification. |
| Product [short] data sheet | Production | This document contains the product specification. |

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

11.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

11.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

Right to make changes — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the

PBSS4240X

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2017. All rights reserved

40 V, 2 A NPN low VCEsat (BISS) transistor

grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of nonautomotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

11.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

40 V, 2 A NPN low VCEsat (BISS) transistor

12. Contents

| 1 | Product profile | 1 |
|------|-------------------------|----|
| 1.1 | General description | 1 |
| 1.2 | Features and benefits | 1 |
| 1.3 | Applications | 1 |
| 1.4 | Quick reference data | 1 |
| 2 | Pinning information | 2 |
| 3 | Ordering information | 2 |
| 4 | Marking | 2 |
| 5 | Limiting values | 2 |
| 6 | Thermal characteristics | 3 |
| 7 | Characteristics | 5 |
| 8 | Package outline | 8 |
| 9 | Soldering | 8 |
| 10 | Revision history | 9 |
| 11 | Legal information | 10 |
| 11.1 | Data sheet status | 10 |
| 11.2 | Definitions | 10 |
| 11.3 | Disclaimers | 10 |
| 11.4 | Trademarks | 11 |

© Nexperia B.V. 2017. All rights reserved

For more information, please visit: http://www.nexperia.com
For sales office addresses, please send an email to: salesaddresses@nexperia.com
Date of release: 15 October 2012

12/12