1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD128 small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 1 A
- Reverse voltage: V_R ≤ 40 V
- · Low forward voltage
- · High power capability due to clip-bonding technology
- Small and flat lead SMD plastic package
- High temperature T_i ≤ 175 °C
- Suitable for both reflow and wave soldering

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- · Switch mode power supply
- · Reverse polarity protection
- · Low power consumption applications
- · High temperature applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{sp} \leq 165 °C	-	-	1	А
V _R	reverse voltage	T _j = 25 °C	-	-	40	V
V _F	forward voltage	I _F = 1 A; T _j = 25 °C	-	430	490	mV
I _R	reverse current	V _R = 40 V; T _j = 25 °C	-	10	50	μA

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		K .JK. A
2	A	anode	1 2 CFP5 (SOD128)	sym001

[1] The marking bar indicates the cathode.



6. Ordering information

Table 3. Ordering information

Type number	Package							
	Name	Description	Version					
PMEG4010ETP		plastic, surface mounted package; 2 terminals; 4 mm pitch; 3.8 mm x 2.6 mm x 1 mm body	SOD128					

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4010ETP	C1

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; $T_{amb} \le$ 145 °C	[1]	-	1	А
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 165 °C		-	1	А
I _{FSM}	non-repetitive peak forward current	t_p = 8.3 ms; half sine wave; $T_{j(init)}$ = 25 °C		-	50	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2]	-	750	mW
			[3]	-	1.25	W
			[1]	-	2.5	W
T _j	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- Device mounted on a ceramic PCB, Al_2O_3 , standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint. Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	[1] [2]	-	-	200	K/W
	junction to ambient		[1] [3]	-	-	120	K/W
			[1] [4]	-	-	60	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[5]	-	-	12	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [4] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [5] Soldering point of cathode tab.

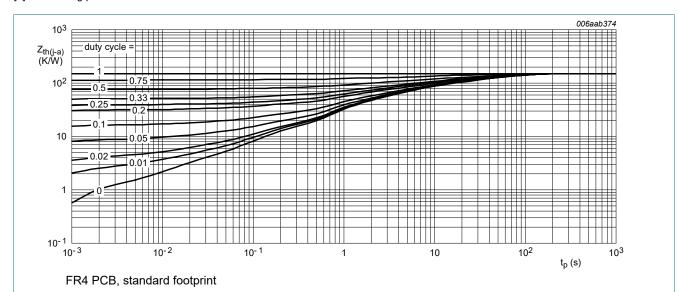


Fig. 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

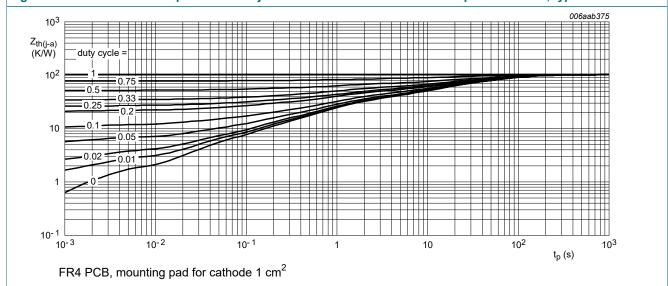
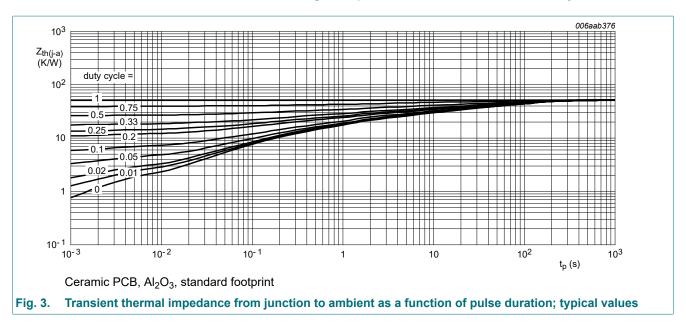


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

High temperature 40 V, 1 A low VF Schottky barrier rectifier

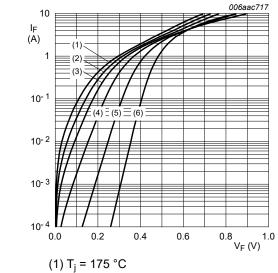


10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 0.1 A; T _j = 25 °C	-	310	360	mV
		I _F = 1 A; T _j = 25 °C	-	430	490	mV
		I _F = 1 A; T _j = 125 °C	-	330	380	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C	-	3	13	μA
		V _R = 40 V; T _j = 25 °C	-	10	50	μA
		V _R = 10 V; T _j = 125 °C	-	2	-	mA
		V _R = 40 V; T _j = 125 °C	-	6	-	mA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	130	-	pF
		$V_R = 10 \text{ V; } f = 1 \text{ MHz; } T_j = 25 \text{ °C}$	-	50	-	pF

High temperature 40 V, 1 A low VF Schottky barrier rectifier



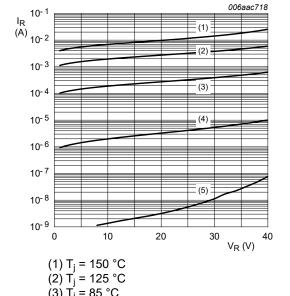
(2) $T_i = 150 °C$

 $(3) T_i = 125 °C$

 $(4) T_i = 85 ^{\circ}C$

(5) $T_i = 25 °C$ (6) $T_i = -40 \, ^{\circ}\text{C}$

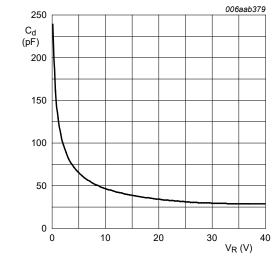
Fig. 4. Forward current as a function of forward voltage; typical values



(3) $T_j = 85 \,^{\circ}\text{C}$

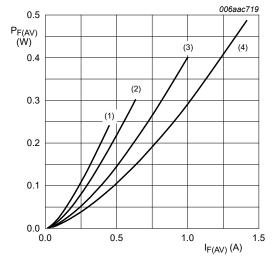
 $(4) T_{j} = 25 ^{\circ}C$ $(5) T_i = -40 ^{\circ}C$

Fig. 5. Reverse current as a function of reverse voltage; typical values



 $f = 1 MHz; T_{amb} = 25 °C$

Diode capacitance as a function of reverse Fig. 6. voltage; typical values



T_i = 175 °C

 $(1) \delta = 0.1$

 $(2) \delta = 0.2$

 $(3) \delta = 0.5$

 $(4) \delta = 1.0$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values

High temperature 40 V, 1 A low VF Schottky barrier rectifier

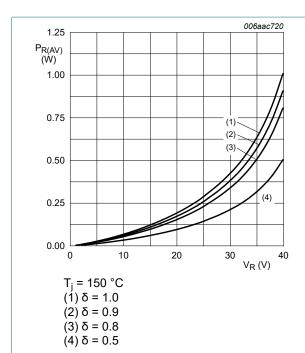
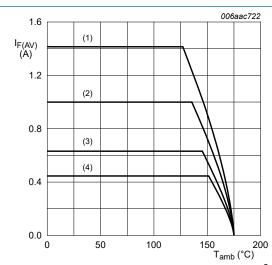


Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, mounting pad for cathode 1 cm²

 $T_j = 175$ °C

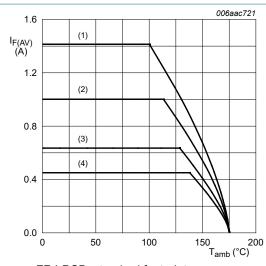
 $(1) \delta = 1.0$

 $(2) \delta = 0.9$

(3) $\delta = 0.8$

 $(4) \delta = 0.5$

Fig. 10. Average forward current as a function of ambient temperature; typical values



FR4 PCB, standard footprint

T_i = 175 °C

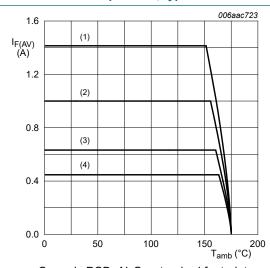
 $(1) \delta = 1.0 (DC)$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 175 °C

 $(1) \delta = 1.0 (DC)$

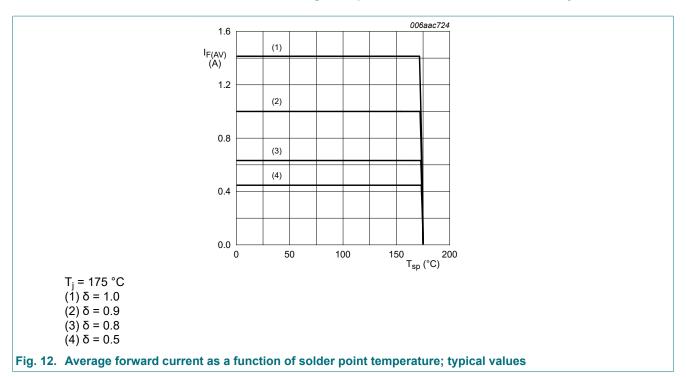
(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

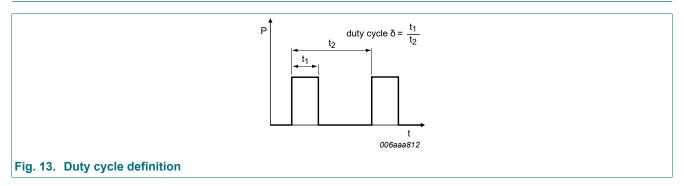
(4) $\delta = 0.1$; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values





11. Test information



The current ratings for the typical waveforms are calculated according to the equations:

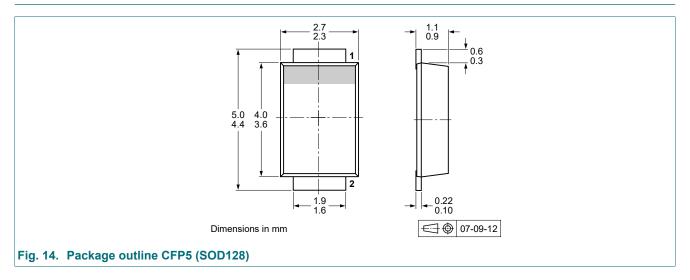
 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

 $I_{RMS}=I_{F(AV)}$ at DC

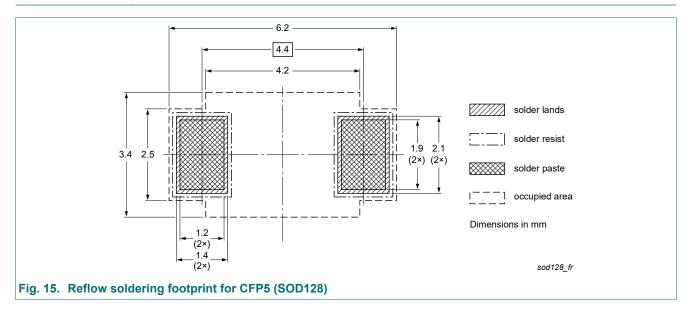
 $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current

High temperature 40 V, 1 A low VF Schottky barrier rectifier

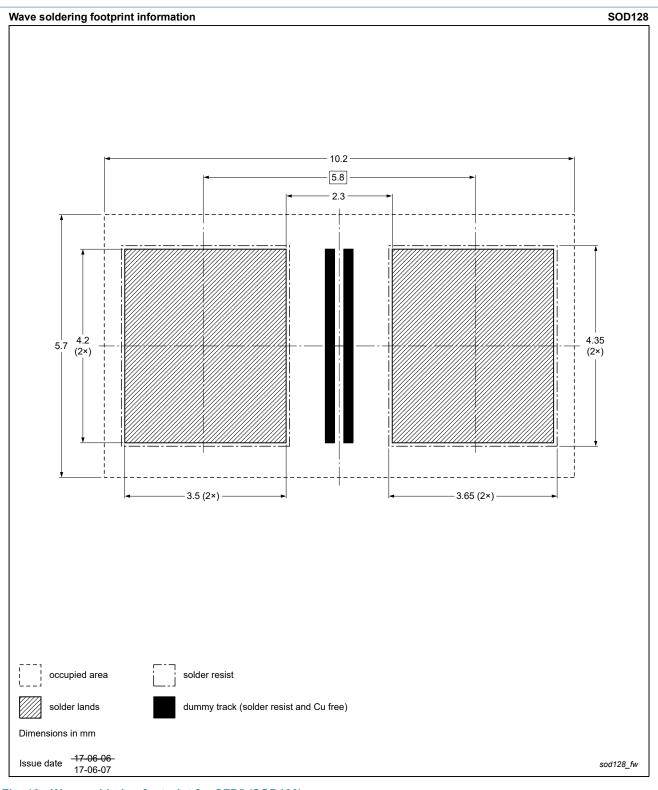
12. Package outline



13. Soldering



High temperature 40 V, 1 A low VF Schottky barrier rectifier



14. Revision history

Table 8. Revision history

Table 6. Nevision history								
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMEG4010ETP v.4	20230220	Product data sheet	-	PMEG4010ETP v.3				
Modifications:	Limiting values: Mea wave.	surement conditions for I	_{FSM} changed from square	e wave to half-sine				
PMEG4010ETP v.3	20230101	Product data sheet	-	PMEG4010ETP v.2				
PMEG4010ETP v.2	20180307	Product data sheet	-	PMEG4010ETP v.1				
PMEG4010ETP v.1	20111005	Product data sheet	-	-				

High temperature 40 V, 1 A low VF Schottky barrier rectifier

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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