

MS40N05

N-Channel 40-V (D-S) MOSFET

Description

The MS40N05 is a high performance trench N-ch MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

The device meets the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

Typical Applications

- Notebook
- Load Switch
- Hand-held Instrument

Package type : SOT-23

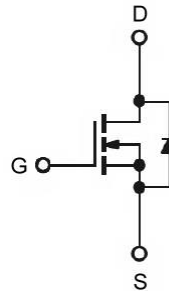
Packing & Order Information

3,000/Reel

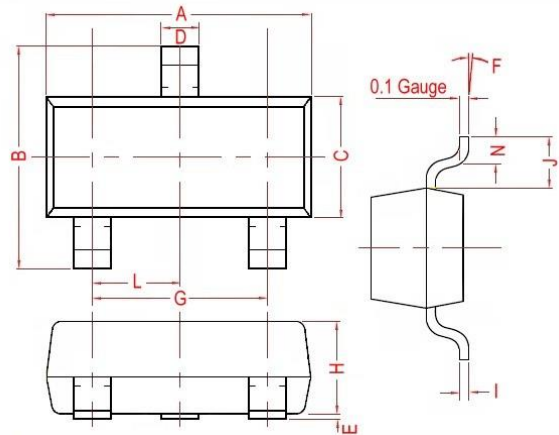


RoHS Compliant

Graphic Symbol

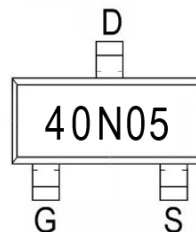


Package Dimension



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	1.90 Ref.	
B	2.30	3.00	H	0.90	1.30
C	1.20	1.75	I	0.05	0.21
D	0.30	0.50	J	0.58 Ref.	
E	0.01	0.15	L	0.95 Typ.	
F	0°	10°	N	0.20 Min.	

Marking



MS40N05

N-Channel 40-V (D-S) MOSFET

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings (unless otherwise specified)

Symbol	Parameter	Value	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ¹ ($T_A = 25^\circ\text{C}$)	5	A
	Continuous Drain Current ¹ ($T_A = 70^\circ\text{C}$)	4.1	A
I_{DM}	Pulsed Drain Current ² ($T_A = 25^\circ\text{C}$)	16	A
P_D	Power Dissipation ³ ($T_A = 25^\circ\text{C}$)	1.25	W
T_J/T_{STG}	Operating Junction and Storage Temperature	-55 to +150	$^\circ\text{C}$

Thermal Resistance Ratings

Symbol	Parameter	Maximum	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient ³	100	$^\circ\text{C/W}$

Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	-	2.5	V
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	40	-	-	V
g_{fs}	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 4\text{A}$	-	12	-	S
I_{GSS}	Gate-Source Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
I_{DSS}	Drain-Source Leakage Current	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1	μA
		$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 55^\circ\text{C}$	-	-	5	μA
$R_{DS(on)}$	Static Drain-Source On-Resistance ²	$V_{GS} = 10\text{V}, I_D = 4.0\text{A}$	-	-	32	m Ω
		$V_{GS} = 4.5\text{V}, I_D = 3.0\text{A}$	-	-	45	m Ω
V_{SD}	Diode Forward Voltage ²	$I_S = 1.0\text{A}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$	-	-	1.2	V
I_S	Continuous Source Current ^{1,4} (Diode)	$V_G = V_D = 0\text{V}, \text{Force Current}$	-	-	5	A
I_{SM}	Pulsed Source Current ^{2,4} (Diode)		-	-	16	

MS40N05

N-Channel 40-V (D-S) MOSFET

Dynamic and switching Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Q _g	Total Gate Charge ²	V _{DS} = 15V	--	5.5	--	nC
Q _{gs}	Gate-Source Charge	I _D = 3A	--	1.25	--	
Q _{gd}	Gate-Drain Charge	V _{GS} = 4.5V	--	2.5	--	
t _{d(on)}	Turn-On Delay Time ²	V _{DS} = 15V	--	8.9	--	ns
t _r	Rise Time	I _D = 1A	--	2.2	--	
t _{d(off)}	Turn-Off Delay Time	V _{GS} = 4.5V	--	41	--	
t _f	Fall Time	R _G = 3.3Ω	--	2.7	--	
C _{ISS}	Input Capacitance	V _{DS} = 15V	--	593	--	pF
C _{OSS}	Output Capacitance	V _{GS} = 0V	--	76	--	
C _{RSS}	Reverse Transfer Capacitance	f = 1.0MHz	--	56	--	

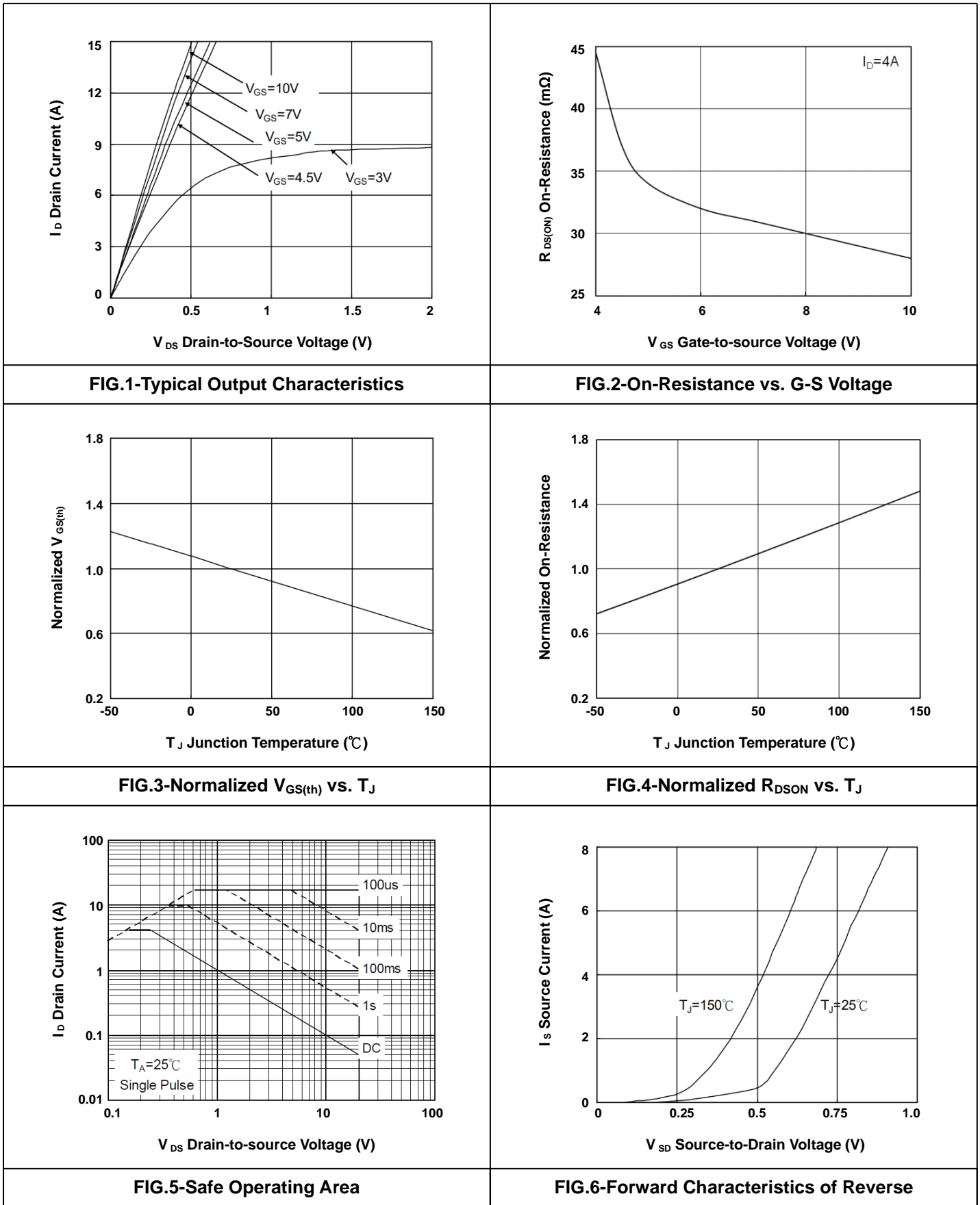
Notes

1. Surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature.
4. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

MS40N05

N-Channel 40-V (D-S) MOSFET

- Typical Electrical Characteristics



MS40N05

N-Channel 40-V (D-S) MOSFET

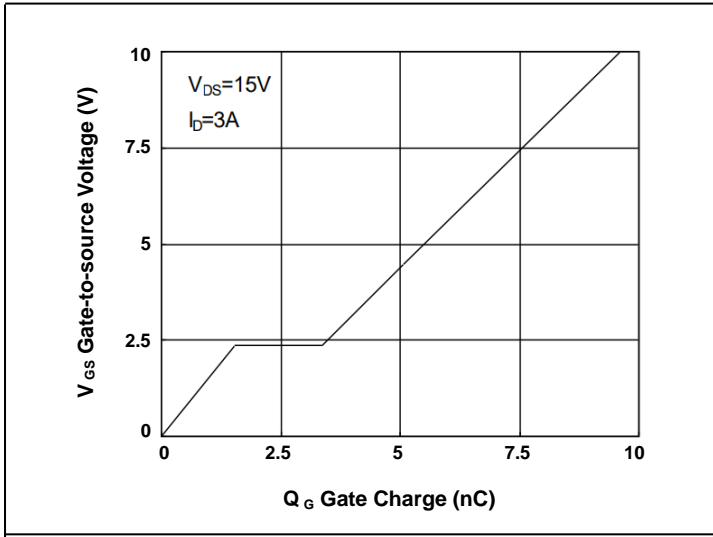


FIG.7-Gate Charge Characteristics

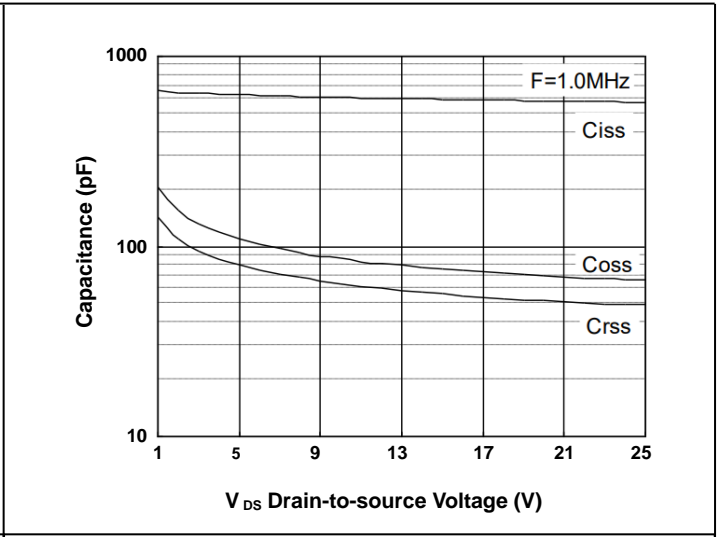


FIG.8-Capacitance Characteristics

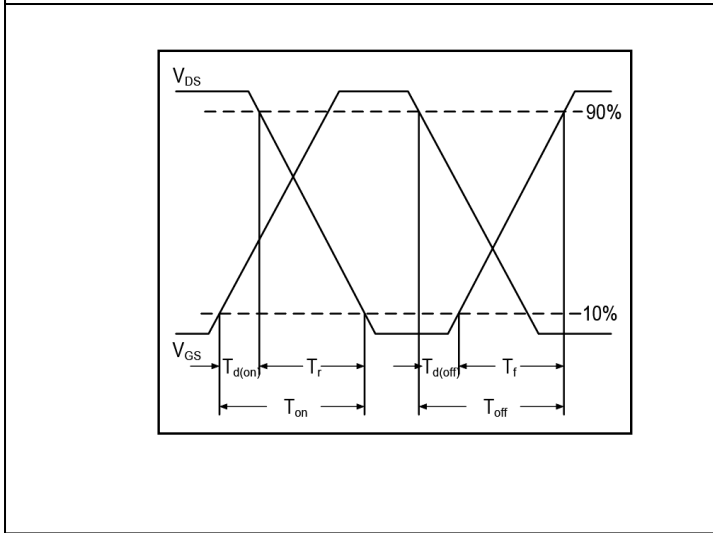


FIG.9-Switching Time Waveform

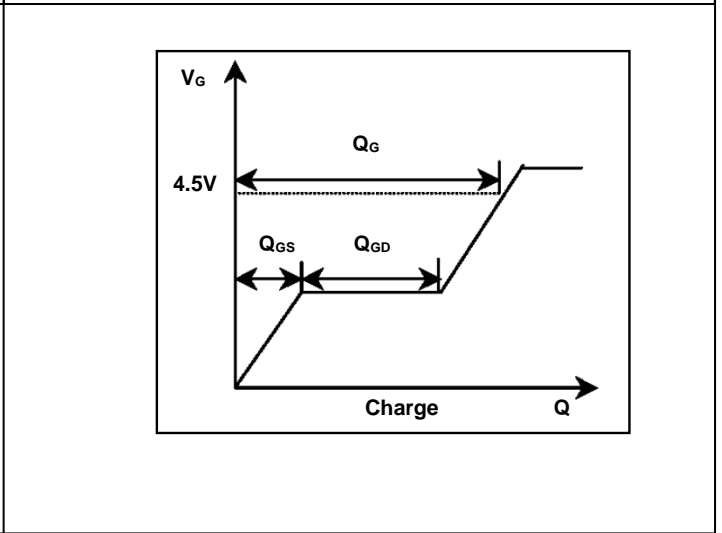


FIG.10-Gate Charge Waveform

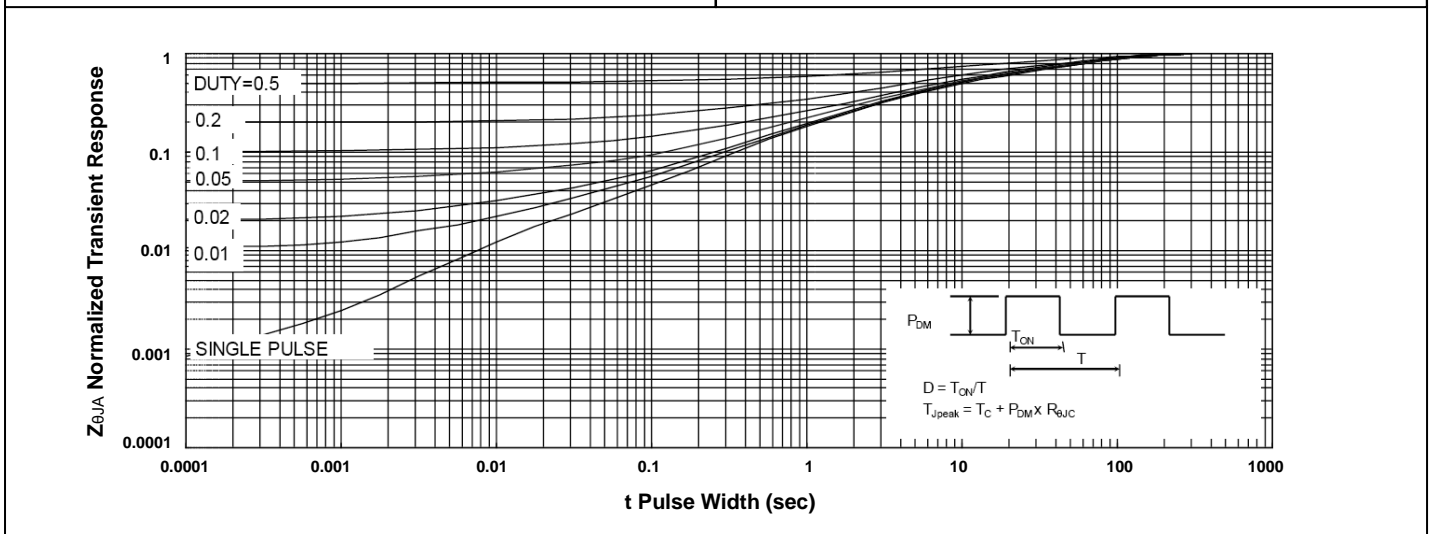


FIG.11-Normalized Maximum Transient Thermal Impedance

MS40N05

N-Channel 40-V (D-S) MOSFET

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE. Bruckewell Technology Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Bruckewell"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product. Bruckewell makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Bruckewell disclaims

- (i) Any and all liability arising out of the application or use of any product.
- (ii) Any and all liability, including without limitation special, consequential or incidental damages.
- (iii) Any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Bruckewell's knowledge of typical requirements that are often placed on Bruckewell products in generic applications.

Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time.

Product specifications do not expand or otherwise modify Bruckewell's terms and conditions of purchase, including but not limited to the warranty expressed therein.