#### **ASDKDVAIG**

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2.5 x 2.0 x 1.0 mm **RoHS/RoHS II Compliant** 

MSL Level = N/A

## **Features**

- Continuous  $V_{DD}$  operation from 1.62 V ~ 3.63 V
- Optimized for low current consumption
- Output Enable/Start & Disable/Stop function
- Output waveform CMOS/LVCMOS compatible
- Hermetically seam-sealed ceramic package
- AEC-Q200 Qualified
- Automotive Grade 1: -40°C to +125°C
- TS16949 Production Line Certified
- PPAP Available Upon Request

## **Applications**

- Infotainment Systems
- Keyless Entry & Startup
- GPS & Navigation
- Comfort control
- ADAS (Advanced Driver Assistance Systems)
- Vehicle to Vehicle Communication
- LiDAR (Light Detection and Ranging)
- In-vehicle Networking
- Powertrain & Drive Control
- Power Control & Conversion
- Industrial Control & Automation

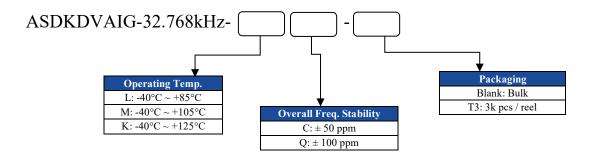
#### **Electrical Specifications**

Parameters		Min.	Тур.	Max.	Units	Notes
Frequency		32.768			kHz	
Operating Temperature Range		-40.0		+85	°C	See options
Storage Temperature Range		-55.0		+125	°C	
Overall Frequency Stability [Note 1]		-100		+100	ppm	See options
Supply Voltage (Vdd)		+1.62		+3.63	V	
Tri-state function [Note 2]		"1" (VIH≥0.7*Vdd) or Open: Oscillation; "0" (VIL<0.3*Vdd): No Oscillation/Hi Z			V	
Output Load				15	рF	CMOS
Output Voltage	$V_{OH}$	0.9*Vdd			V	
Output voltage	$V_{OL}$			0.1*Vdd		
Aging 1 year @25°C±3°C		-3.0		+3.0	ppm	
Aging 5 years @25°C± 3°C		-5.0		+5.0	ppm	
Symmetry @ ½ Vdd		45	50	55	%	
Start-up Time				30	ms	
Rise and Fall Time (Tr/Tf) @10%Vdd-90%Vdd, 15pF load				30	ns	
Disable Current				5	μA	
Supply Current (Idd) @25°C± 3°C	Vdd = 3.3V		10	15	μA	No Load
	Vdd = 2.5V		9	14	μA	No Load
	Vdd = 1.8V		8	13	μA	No Load

Overall frequency stability includes initial frequency tolerance @25°C±3°C and stability over the operating temperature range.

Do not leave pin 1 ( $\overline{\text{INH}}$ ) floating. If pin 1 ( $\overline{\text{INH}}$ ) is not utilized for toggling, it must be tied to Vdd (logic 1).

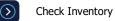
#### **Part Identification**





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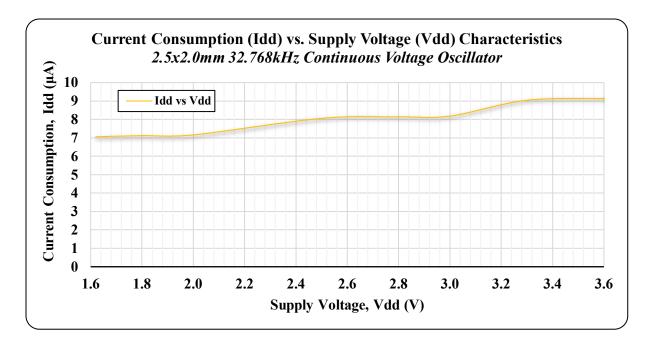


2.5 x 2.0 x 1.0 mm RoHS/RoHS II Compliant MSL Level = N/A

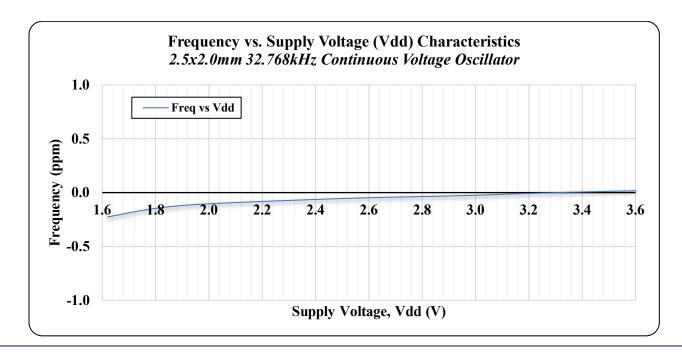




Typical Current Consumption (Idd) vs. Supply Voltage (Vdd) Characteristics [@ 25°C±3°C, No Load]



Typical Frequency vs. Supply Voltage (Vdd) Characteristics [@ 25°C± 3°C, Normalized to Vdd=3.3Vdc]





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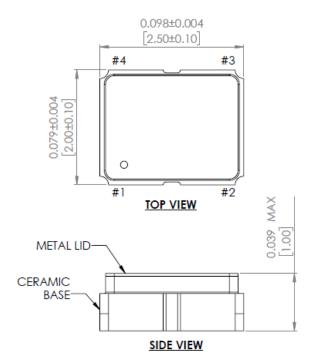


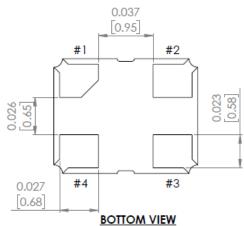
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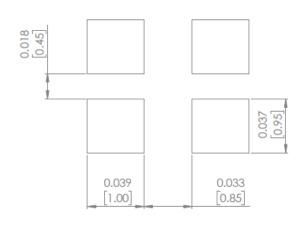


#### **Mechanical Dimensions**





#### Recommended Land Pattern



Pin #	Function
1	ĪNH
2	GND
3	Output
4	$V_{\mathrm{DD}}$

INH Function				
#1	#3 (Output)			
Open	Active			
"H" Level	Active			
"L" Level	High Z (No Oscillation)			

#### Note 3:

- -Do not leave Pin 1 (INH) floating
- -If Pin 1 (INH) is not utilized for toggling, it must be tied to Vdd (logic 1)

#### Note 4:

Recommended to use approximately  $0.01 \mu F$  bypass capacitor between PIN 2 and PIN 4

**Dimensions: inches (mm)** 



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ESD Sensitive



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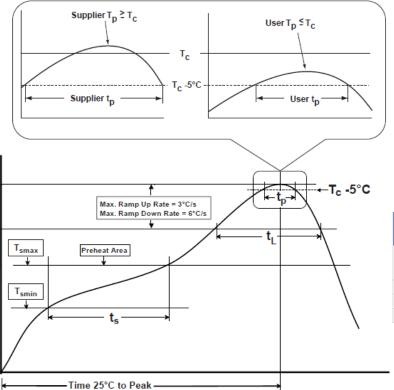
## **Reflow Profile [JDEC J-STD-020]**

T<sub>p</sub>.

 $T_L$ 

25

Temperature



# Table 1 SnPb Eutectic Process Classification Temperatures (Tc) Package Thickness Volume mm³ <350</td> ≥350 <2.5 mm</td> 235 °C 220 °C ≥2.5 mm 220 °C 220 °C

Table 2
Pb-Free Process

Classification Temperatures (1c)					
Package Thickness	Volume mm³ <350	Volume mm <sup>3</sup> 350-2000	Volume mm³ >2000		
<1.6 mm	260 °C	260 °C	260 °C		
1.6 mm - 2.5 mm	260 °C	250 °C	245 °C		
>2.5 mm	250 °C	245 °C	245 °C		

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat / soak		
Temperature minimum (T <sub>smin</sub> )	100°C	150°C
Temperature maximum (T <sub>smax</sub> )	150°C	200°C
Time $(T_{smin} \text{ to } T_{smax})$ $(t_s)$	60 - 120 sec.	60 - 120 sec.
Average ramp-up rate $(T_{smax} \text{ to } T_P)$	3°C/sec. max	3°C/sec. max
Liquidous temperature (T <sub>L</sub> )	183°C	217°C
Time at liquidous (t <sub>L</sub> )	60 - 150 sec.	60 - 150 sec.
Peak package body temperature (T <sub>P</sub> )*	see Table 1	see Table 2
Time (t <sub>p</sub> )** within 5°C of the specified classification temperature (T <sub>C</sub> )	20 sec.	30 sec.
Ramp-down rate (T <sub>p</sub> to T <sub>smax</sub> )	6°C/sec. max	6°C/sec. max
Time 25°C to peak temperature	6 min. max	8 min. max

<sup>\*</sup>Tolerance for peak profile temperature (T<sub>P</sub>) is defined as a supplier minimum and a user maximum.



<sup>\*\*</sup>Tolerance for time at peak profile temperature  $(t_p)$  is defined as supplier minimum and a user maximum.

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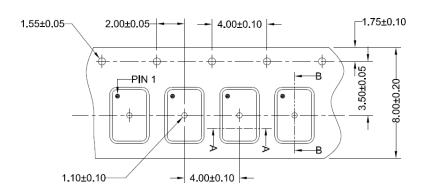
ESD Sensitive

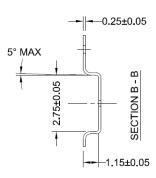


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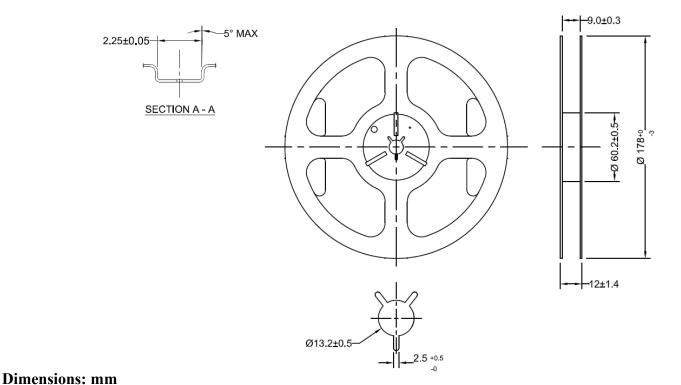
## **Packaging**

## T3: 3,000pcs/reel





FEEDING (PULL) DIRECTION



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